

Docket No.: WMP-IFT-823

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : ZENKO GERGINTSCHEW
Filed : CONCURRENTLY HEREWITH
Title : METHOD FOR DRIVING A SEMICONDUCTOR SWITCH AND
CIRCUIT CONFIGURATION WITH A SEMICONDUCTOR
SWITCH

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

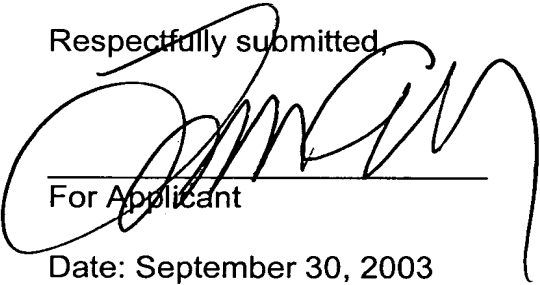
Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent No. 5,187,632 (Blessing), dated February 16, 1993;

European Patent Application EP 0 373 694 A1 (Palara), dated June 20, 1990.

Respectfully submitted,



For Applicant

LAURENCE A. GREENBERG
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Date: September 30, 2003

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FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Attorney Docket No.: WMP-IFT-823 Appl. No.: <hr/> Applicant: ZENKO GERGINTSCHEW <hr/> Filing Date: September 30, 2003 Group Art Unit:			
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	5,187,632	2/16/93	Blessing			
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
FOREIGN PATENT DOCUMENT							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J	0 373 694 A1	6/20/90	Europe			
	K						
	L						
	M						
	N						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
EXAMINER				DATE CONSIDERED			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							